

**Package List**

LIBRARY ieee;  
 USE ieee.std\_logic\_1164.all;  
 USE ieee.std\_logic\_arith.all;  
 USE ieee.STD\_LOGIC\_UNSIGNED.all;

**Declarations**

**Ports:**

```

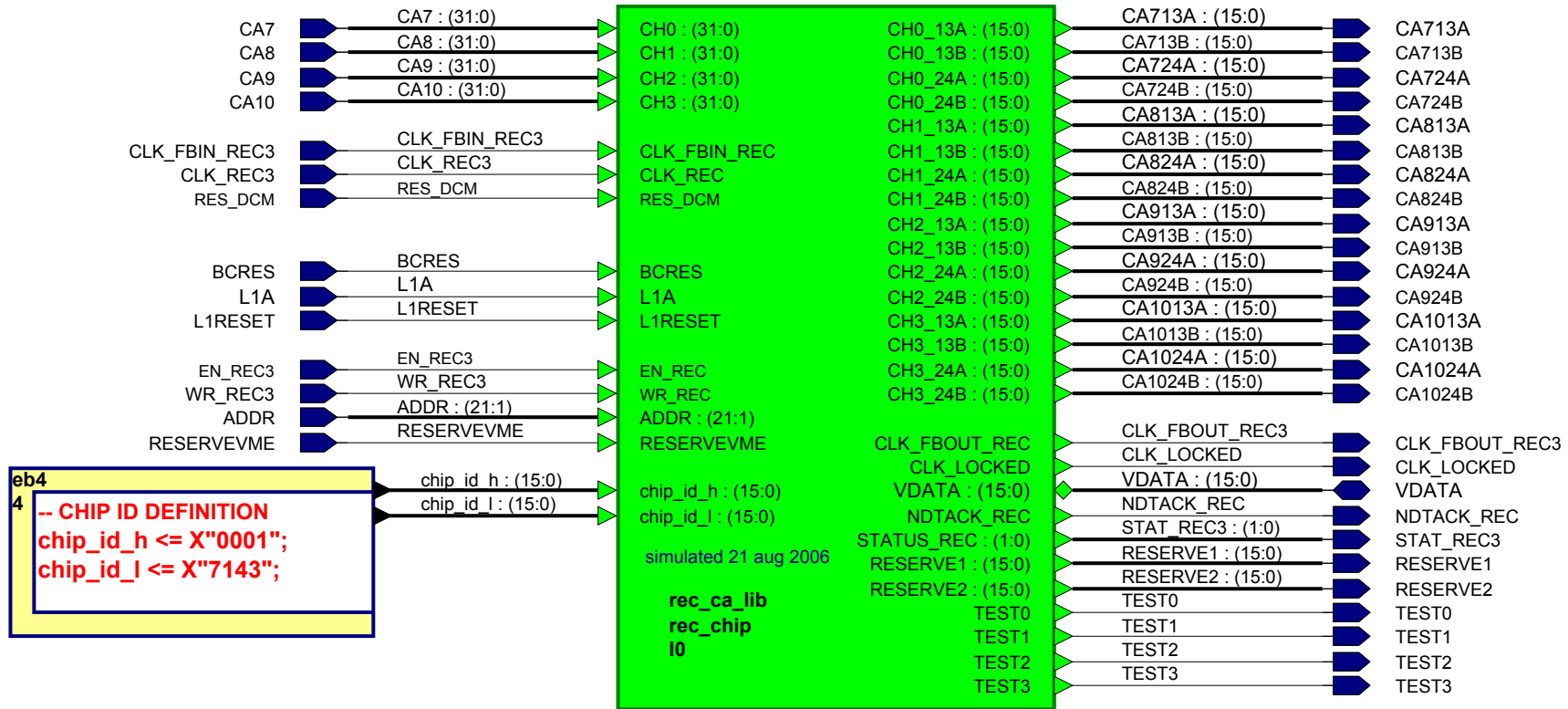
ADDR          : std_logic_vector(21 DOWNTO 1)
BCRES         : std_logic
CA10          : std_logic_vector(31 DOWNTO 0)
CA7           : std_logic_vector(31 DOWNTO 0)
CA8           : std_logic_vector(31 DOWNTO 0)
CA9           : std_logic_vector(31 DOWNTO 0)
CLK_FBIN_REC3 : std_logic
CLK_REC3      : std_logic
EN_REC3       : std_logic
L1A           : std_logic
L1RESET       : std_logic
RESERVEVME    : std_logic
RES_DCM       : std_logic
WR_REC3       : std_logic
CA1013A       : std_logic_vector(15 DOWNTO 0)
CA1013B       : std_logic_vector(15 DOWNTO 0)
CA1024A       : std_logic_vector(15 DOWNTO 0)
CA1024B       : std_logic_vector(15 DOWNTO 0)
CA713A        : std_logic_vector(15 DOWNTO 0)
CA713B        : std_logic_vector(15 DOWNTO 0)
CA724A        : std_logic_vector(15 DOWNTO 0)
CA724B        : std_logic_vector(15 DOWNTO 0)
CA813A        : std_logic_vector(15 DOWNTO 0)
CA813B        : std_logic_vector(15 DOWNTO 0)
CA824A        : std_logic_vector(15 DOWNTO 0)
CA824B        : std_logic_vector(15 DOWNTO 0)
CA913A        : std_logic_vector(15 DOWNTO 0)
CA913B        : std_logic_vector(15 DOWNTO 0)
CA924A        : std_logic_vector(15 DOWNTO 0)
CA924B        : std_logic_vector(15 DOWNTO 0)
CLK_FBOUT_REC3 : std_logic
CLK_LOCKED    : std_logic
NDTACK_REC    : std_logic
RESERVE1      : std_logic_vector(15 DOWNTO 0)
RESERVE2      : std_logic_vector(15 DOWNTO 0)
STAT_REC3     : std_logic_vector(1 DOWNTO 0)
TEST0         : std_logic
TEST1         : std_logic
TEST2         : std_logic
TEST3         : std_logic
VDATA        : std_logic_vector(15 DOWNTO 0)
    
```

**Diagram Signals:**

```

SIGNAL chip_id_h : std_logic_vector(15 DOWNTO 0)
SIGNAL chip_id_l : std_logic_vector(15 DOWNTO 0)
    
```

<company name>		Project:	rec_ca
		<enter comments here>	
Title:	<enter diagram title here>		
Path:	rec_ca_lib/rec3_chip/struct		
Edited:	by taurok on 23 Aug 2006		



**WRAPPING FILE for REC3 chip**

**REC3 chip**