

GTL_JTAG

chain	device	device status	jumper settings	configuration technique	configuration conditions	remarks
ALTERA	VME64x-chip	OUT	JP40/2-3	-	JP38 = "OUT" for JTAGoverVME	
	PROM VME64x	IN	JP41/1-2	MasterBlaster		
	VME_GTL-chip	OUT	JP42/2-3	-		
	PROM VME_GTL	IN	JP43/1-2	MasterBlaster		
	PROM COND1	IN	JP44/1-2	JTAGoverVME		
	COND1-chip	OUT	JP45/2-3	-		
	PROM COND2	IN	JP46/1-2	JTAGoverVME		
	COND2-chip	OUT	JP47/2-3	-		
XILINX	1. PROM REC3	IN	JP49/1-2	JTAGoverVME		
	2. PROM REC3	IN	JP50/1-2	JTAGoverVME		
	3. PROM REC3	IN	JP51/1-2	JTAGoverVME		
	4. PROM REC3	IN	JP52/1-2	JTAGoverVME		
	REC3-chip	IN	JP48/1-2	-		no TMS jumper
	1. PROM REC2	IN	JP53/1-2	JTAGoverVME		
	2. PROM REC2	IN	JP54/1-2	JTAGoverVME		
	3. PROM REC2	IN	JP55/1-2	JTAGoverVME		
	4. PROM REC2	IN	JP56/1-2	JTAGoverVME		
	REC2-chip	IN	JP57/1-2	-		no TMS jumper
	1. PROM REC1	IN	JP59/1-2	JTAGoverVME		
	2. PROM REC1	IN	JP60/1-2	JTAGoverVME		
	3. PROM REC1	IN	JP61/1-2	JTAGoverVME		
	4. PROM REC1	IN	JP62/1-2	JTAGoverVME		
REC1-chip	IN	JP58/1-2	-		no TMS jumper	