

GTFE_JTAG

chain	device	device status	jumper settings	configuration technique	configuration conditions	remarks
ALTERA	VME64x-chip	OUT	JP4/2-3	-	JP39 = "IN" for MasterBlaster	
	VME_GTFE-chip	OUT	JP6/2-3	-		
	PROM VME64x	IN	JP5/1-2	MasterBlaster		
	PROM VME_GTFE	IN	JP7/1-2	MasterBlaster		
XILINX	1. PROM DAQ	IN	JP53/1-2	JTAGoverVME	JP39 = "OUT" for JTAGoverVME	
	2. PROM DAQ	IN	JP51/1-2	JTAGoverVME		
	3. PROM DAQ	IN	JP52/1-2	JTAGoverVME		
	4. PROM DAQ	IN	JP50/1-2	JTAGoverVME		
	DAQ-chip	OUT	JP54/2-3	-		
	1. PROM EVM	IN	JP46/1-2	JTAGoverVME		
	2. PROM EVM	IN	JP48/1-2	JTAGoverVME		
	3. PROM EVM	IN	JP47/1-2	JTAGoverVME		
	4. PROM EVM	IN	JP49/1-2	JTAGoverVME		
	EVM-chip	OUT	JP55/2-3	-		
	TTCRQ-mezzanine	OUT	JP41/2-3	-		
	SLINK-EVM-mezzanine	OUT	JP42/2-3	-		
	SLINK-DAQ-mezzanine	OUT	JP43/2-3	-		