

GMT_JTAG

chain	device	device status	jumper	setting	configuration technique	configuration conditions	remarks
ALTERA	VME64x-chip	OUT	JP4	2-3	-	JP136, JP147, JP150 = "IN"; JP115, JP145, JP146, JP148, JP149, JP151 = "OUT" for MasterBlaster	
	PROM VME64x	IN	JP6	1-2	MasterBlaster		
XILINX	1. PROM AUF	IN	JP76	1-2	JTAGoverVME	JP36, JP85, JP114 = "IN"; JP37, JP84, JP94, JP95, JP104, JP105 = "OUT" for JTAGoverVME	
	2. PROM AUF	IN	JP78	1-2	JTAGoverVME		
	3. PROM AUF	IN	JP77	1-2	JTAGoverVME		
	4. PROM AUF	IN	JP79	1-2	JTAGoverVME		
	AUF-chip	OUT	JP75	2-3	-		
	1. PROM SRT	IN	JP47	1-2	JTAGoverVME		
	2. PROM SRT	IN	JP46	1-2	JTAGoverVME		
	3. PROM SRT	IN	JP44	1-2	JTAGoverVME		
	SRT-chip	OUT	JP48	2-3	-		
	1. PROM ROP	IN	JP8	1-2	JTAGoverVME		
	2. PROM ROP	IN	JP9	1-2	JTAGoverVME		
	3. PROM ROP	IN	JP10	1-2	JTAGoverVME		
	ROP-chip	OUT	JP12	2-3	-		
	1. PROM AUB	IN	JP41	1-2	JTAGoverVME		
	2. PROM AUB	IN	JP40	1-2	JTAGoverVME		
	3. PROM AUB	IN	JP39	1-2	JTAGoverVME		
	4. PROM AUB	IN	JP38	1-2	JTAGoverVME		
	AUB-chip	OUT	JP42	2-3	-		
	1. PROM INB	IN	JP55	1-2	JTAGoverVME		
	2. PROM INB	IN	JP54	1-2	JTAGoverVME		
3. PROM INB	IN	JP53	1-2	JTAGoverVME			
INB-chip	OUT	JP56	2-3	-			
1. PROM IND	IN	JP51	1-2	JTAGoverVME			

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	2. PROM IND	IN	JP50	1-2	JTAGoverVME		
	3. PROM IND	IN	JP49	1-2	JTAGoverVME		
	IND-chip	OUT	JP52	2-3	-		
	1. PROM LFB	IN	JP67	1-2	JTAGoverVME		
	2. PROM LFB	IN	JP69	1-2	JTAGoverVME		
	3. PROM LFB	IN	JP68	1-2	JTAGoverVME		
	4. PROM LFB	IN	JP70	1-2	JTAGoverVME		
	LFB-chip	OUT	JP66	2-3	-		
	1. PROM LFF	IN	JP62	1-2	JTAGoverVME		
	2. PROM LFF	IN	JP64	1-2	JTAGoverVME		
	3. PROM LFF	IN	JP63	1-2	JTAGoverVME		
	4. PROM LFF	IN	JP65	1-2	JTAGoverVME		
	LFF-chip	OUT	JP61	2-3	-		
	1. PROM INC	IN	JP58	1-2	JTAGoverVME		
	2. PROM INC	IN	JP60	1-2	JTAGoverVME		
	3. PROM INC	IN	JP59	1-2	JTAGoverVME		
	INC-chip	OUT	JP57	2-3	-		
	1. PROM INF	IN	JP72	1-2	JTAGoverVME		
	2. PROM INF	IN	JP74	1-2	JTAGoverVME		
	3. PROM INF	IN	JP73	1-2	JTAGoverVME		
	INF-chip	OUT	JP71	2-3	-		