

FDL_JTAG

chain	device	device status	jumper settings	configuration technique	configuration conditions	remarks
ALTERA	VME64x-chip	IN	JP4/1-2	MasterBlaster	SW4/pin8 = "ON" for MasterBlaster	no JTAGoverVME for ALTERA chips. All ALTERA in chain, because no TMS jumper.
	VME_FDL-chip	IN	JP6/1-2	MasterBlaster		
	PROM VME64x	IN	JP5/1-2	MasterBlaster		
	PROM VME_FDL	IN	JP7/1-2	MasterBlaster		
XILINX	1. PROM FDL	IN	JP8/1-2	JTAGoverVME	JP13-JP16 on 1-2 and SW4/7 = "OFF" for JTAGoverVME	
	2. PROM FDL	IN	JP9/1-2	JTAGoverVME		
	3. PROM FDL	IN	JP10/1-2	JTAGoverVME		
	4. PROM FDL	IN	JP11/1-2	JTAGoverVME		
	FDL-chip	OUT	JP12/2-3	-		